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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re application of:

CHAD A. COBBLEY
TONGBI JIANG
ED A. SCHROCK

Filed: AUGUST 22, 1997

For: SEMICONDUCTOR DIE ATTACHMENT
METHOD AND APPARATUS

Attorney Docket No.: 97-0098

TRANSMITTAL LETTER
August 22, 1997

BOX PATENT APPLICATION
Commissioner of Patents and Trademarks
Washington, D.C. 20231

Sir:

Enclosed for filing is the patent application of CHAD A. COBBLEY, TONGBI JIANG, and ED A. SCHROCK entitled "SEMICONDUCTOR DIE ATTACHMENT METHOD AND APPARATUS" comprising 21 pages of cover page, specification, claims and Abstract. Also enclosed are:

- x Two sheets of drawings (Figures 1A-6).
- x A Certificate of Mailing by "Express Mail" by use of Express Mail Label No. EI 128 114 513 US certifying a deposit date of August 22, 1997.
- x A Declaration and Power of Attorney for Patent Application (Joint Inventors).
- x An Assignment of the invention to MICRON TECHNOLOGY, INC., along with Form PTO-1595 attached.
- x A check in the amount of \$40.00 to cover the recording fee for the Assignment (\$40.00; 37 C.F.R. 1.21(h)).
- x An Information Disclosure Statement Under 37 C.F.R. §1.97 and §1.98. Copies of all references listed on Form PTO-1449 are enclosed herewith.
- x A return receipt postcard.

x A check in the amount of \$1968.00 is enclosed for the filing fee.

Fee Calculation (37 CFR 1.16)

Claims as filed:

						OTHER THAN A
						SMALL ENTITY
FOR:	# FILED	# EXTRA	RATE	FEE	or	RATE FEE
BASIC FEE			\$385	\$		\$770 = \$ 770
TOT. CLMS.	49 - 20 = 29		x11 =	\$	x \$ 22 =	\$ 638
INDEP.	10 - 3 = 7		x40 =	\$	x \$ 80 =	\$ 560
TOTAL ADDITIONAL FEE				\$		\$1968

 x The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 07-1857, pertaining to i) any filing fees under 37 CFR 1.16 for the presentation of extra claims; ii) any patent application processing fees under 37 CFR 1.17. Copies in triplicate of this sheet are enclosed.

DATED this 22nd day of August, 1997.

RESPECTFULLY SUBMITTED:



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August 22, 1997
Date of Signature


Stephen A. Gratton, Attorney for Applicants

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Date of Deposit: AUGUST 22, 1997

CERTIFICATE OF MAILING BY EXPRESS MAIL
UNDER 37 C.F.R. §1.10

I hereby certify that this patent application in the names of CHAD A. COBBLEY, TONGBI JIANG, and ED A. SCHROCK entitled "SEMICONDUCTOR DIE ATTACHMENT METHOD AND APPARATUS" comprising pages 1-21 of cover sheet, specification, claims and Abstract; two sheets of drawings (Figures 1A-6); a Transmittal Letter with copies in triplicate; a check in the amount of \$1968.00 for the filing fee; a Declaration and Power of Attorney for Patent Application (Joint Inventors); an Assignment of the invention to MICRON TECHNOLOGY, INC. along with PTO Cover Sheet Form 1595 attached; a check in the amount of \$40.00 for the fee to record the Assignment; an Information Disclosure Statement Under 37 C.F.R. §1.97 and §1.98 along with copies of all cited references; and a return postcard, are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. §1.10 on the date indicated above in an envelope addressed to the Commissioner of Patents and Trademarks, Box Patent Application, Washington, D.C. 20231.

DATED this 22nd day of August, 1997.

August 22, 1997
Date of Signature

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August 22, 1997
Date of Signature


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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICATION FOR LETTERS PATENT

**SEMICONDUCTOR DIE ATTACHMENT
METHOD AND APPARATUS**

INVENTORS

**CHAD A. COBBLEY
TONGBI JIANG
ED A. SCHROCK**

ATTORNEY'S DOCKET NO. 97-0098

Field of the Invention

This invention relates to a method and system for attaching semiconductor dice to a leadframe, and more particularly to such a method and system wherein an instant
5 curing adhesive, such as cyanoacrylate is utilized to bond the dice to the leadframe.

Background of the Invention

10 A conventional semiconductor package is formed by attaching several semiconductor dice to a metal leadframe. The leadframe comprises a metal strip that supports the dice for packaging and provides the leads in the completed
15 packages. One type of leadframe includes several mounting paddles for attaching each die to the leadframe. Each paddle has a peripheral configuration which corresponds to the peripheral configuration of a semiconductor die. The leadframe also includes lead fingers that can be wire bonded
20 to the bond pads on the dice. Following attachment and wire bonding of the dice to the leadframe, the dice and mounting paddles can be encapsulated in plastic, and the lead fingers trimmed to form the package leads.

Another type of leadframe is known as a lead-on-chip (LOC) leadframe. A LOC leadframe does not include mounting
25 paddles for the dice. With a LOC leadframe, the lead fingers not only form the leads for the completed package, but also support the dice during the encapsulation process. Typical lead-on-chip (LOC) dice include bond pads formed along the center of the die. The dice can be attached directly to die
30 attach areas on the lead fingers, and the bond pads on the dice wire bonded to bonding sites on the lead fingers.

Die attachment to the leadframe is designed to create a strong bond between the dice and the packages, the integrity of which can withstand subsequent processing and use in an
35 electronic product. In addition, the material used for attaching the dice to the leadframe can serve other purposes

such as heat transfer, and in some cases, electrical conductivity between the dice and lead fingers.

One conventional method of attaching dice to a leadframe is with polymer adhesives, such as epoxy and polyimide.

5 Polymer adhesives are commercially available as viscous materials which can be deposited on the mounting paddles of the leadframe, such as by dispensing with a syringe, or screen printing. Using a die attach mechanism, each die can be positioned over a die attach area, then pressed onto the
10 leadframe to form a thin adhesive layer between the die and the leadframe. Finally, the leadframe and die can be placed in an oven, to cure the adhesive at elevated temperatures in an inert atmosphere. Typical curing temperatures are from 150°C to 275°C for thirty minutes to one hour, in a nitrogen
15 atmosphere.

For attaching LOC dice to a LOC leadframe, a double-sided adhesive tape can be used in place of a dispensed adhesive. The adhesive tape can include a thermoset adhesive on both sides, such as an epoxy, acrylic, silicone or
20 polyimide. Such LOC tape is initially attached to the leadframe, and then to the faces of the dice using heat and pressure. Finally, the tape can be cured by subjecting the packages to elevated temperature in an oven.

Each semiconductor die has a thermal budget during
25 processing and packaging, in that each die may only be subjected to a certain amount of heat without inducing thermal stresses. These thermal stresses can lead to loss of physical integrity or processing function in a die. It is generally accepted that the amount of heat to which dice are
30 subjected during processing and packaging should be minimized. Thus, the use of heat curable adhesives for attaching semiconductor dice to leadframes during packaging are problematic. It would be advantageous to be able to attach semiconductor dice to leadframes without heating and
35 introducing thermal stresses on the dice. It would also be

advantageous to be able to attach semiconductor dice to leadframes at room temperature and ambient atmosphere.

The present invention is directed to a method and system for attaching semiconductor dice to leadframes, including LOC leadframes, without inducing thermal stresses on the dice. Still further, the present invention is directed to a method and system for attaching semiconductor dice to leadframes, including LOC leadframes, which can be performed in-line at room temperature, and ambient atmosphere with a high throughput.

Summary of the Invention

In accordance with the present invention, an improved method for attaching semiconductor dice to leadframes, an improved semiconductor package, and a system for performing the method are provided. The method, simply stated, comprises: applying an instant curing adhesive to a portion of a leadframe; placing a semiconductor die in contact with the adhesive; and then polymerizing the adhesive at room temperature and ambient atmosphere to form a cured adhesive layer and bond the die to the leadframe.

Suitable instant curing adhesives include cyanoacrylate adhesives, and anaerobic acrylic adhesives. Commercial formulations of these adhesives can be modified to tailor the characteristics of the cured adhesive layer to electronics packaging. These characteristics can include improved mechanical strength, heat conductivity, moisture resistivity, and thermostability. In addition, depending on the application, the instant curing adhesive can be formulated with an increased electrical conductivity or alternately with an increased dielectric strength.

Following curing of the adhesive, and bonding of the die to the leadframe, the die can be wire bonded to the leadframe and encapsulated in a polymer, and the leadframe trimmed to form a singulated semiconductor package. The semiconductor

package comprises a trimmed portion of leadframe, the encapsulated semiconductor die, and the cured adhesive layer.

The method can be performed with a conventional lead frame having mounting paddles, or with a lead-on-chip (LOC) lead frame having lead fingers configured to support the die for packaging. With a conventional leadframe, the adhesive can include conductive fillers selected to improve the electrical conductivity and heat conductivity of the cured adhesive layer. With a LOC leadframe, the adhesive can include non-conductive fillers selected to increase the dielectric strength of the cured adhesive layer and prevent cross-talk between the lead fingers.

The system comprises: a dispensing mechanism for dispensing the instant curing adhesive on the leadframe; and a die attach mechanism for positioning and placing the die in contact with the dispensed adhesive. In the illustrative embodiment, the die attach mechanism comprises a conventional LOC die attacher modified to practice the invention.

Brief Description of the Drawings

Figure 1A is a schematic plan view with parts removed of a leadframe having a semiconductor die attached thereto in accordance with the invention;

Figure 1B is a cross sectional view taken along section line 1B-1B of Figure 1A illustrating a semiconductor package constructed in accordance with the invention;

Figure 2 is a block diagram illustrating broad steps in the method of the invention;

Figure 3A is a schematic plan view of the semiconductor die illustrating an instant curing adhesive dispensed thereon in a dot pattern;

Figure 3B is a schematic plan view of a mounting paddle of the leadframe illustrating an instant curing adhesive dispensed thereon in a linear pattern;

Figure 4 is a schematic block diagram of a system adapted to perform the method outlined in Figure 2;

Figure 5 is a perspective view of a LOC leadframe and a LOC semiconductor die being attached in accordance with the method of the invention using an instant curing adhesive material; and

Figure 6 is a side elevation view of the LOC leadframe showing LOC semiconductor dice and associated adhesive layers prior to encapsulation and singulation.

Detailed Description of the Preferred Embodiment

Referring to Figure 1A, a semiconductor die 10 is shown attached to a leadframe 14 in accordance with the method of the invention. The semiconductor die 10 includes a pattern of bond pads 18 in electrical communication with the integrated circuits contained on the die 10. The leadframe 14 comprises a conventionally formed metal leadframe having mounting paddles 12 and paddle support bars 13. In addition, the leadframe 14 includes an arrangement of lead fingers 16. Wires 15 are bonded to the bond pads 18 on the die 10, and to bonding sites on the lead fingers 16 to form separate electrical paths therebetween.

Figure 1B illustrates a semiconductor package 17 after singulation from the lead frame 14. As shown in Figure 1B, the package 17 includes the die 10, which has been attached to the mounting paddle 12 using a cured adhesive layer 20. Further details of the attachment process will be hereinafter described. The semiconductor package 17 also includes a plastic encapsulant 19, which encapsulates the die 10 and portions of the lead fingers 16A. Leads 16A for the package 17 comprise trimmed and formed portions of the lead fingers 16 (Figure 1A).

Referring to Figure 2, broad steps in a method for attaching the die 10 (Figure 1A) to the leadframe 14 (Figure

1B) to form the semiconductor package 17 (Figure 1B) are illustrated. These broad steps include:

Providing a conventional semiconductor die and leadframe, step A.

5 Dispensing an instant curing adhesive material on the leadframe (or alternately on the die), step B.

Placing the die in contact with the adhesive material, step C.

10 Polymerizing the adhesive material to form a cured adhesive layer and bond the die to the leadframe, step D.

Step A can be performed using the conventional die 10 and leadframe 14 illustrated in Figure 1A.

15 Step B is illustrated in Figures 3A and 3B. In Figure 3A, adhesive material in the form of a pattern of circular adhesive dots 20A has been applied to a backside 21 of the semiconductor die 10 prior to placing the die 10 in contact with the mounting paddle 12. Alternately, as shown in Figure 3B, adhesive material in the form of a pattern of elongated adhesive lines 20B has been applied to the mounting paddle 12. As is apparent, the patterns of dots 20A and lines 20B are merely exemplary and other patterns, including uniform and random patterns, can be employed.

20 The adhesive material can be applied by any suitable in-line dispensing mechanism including syringe dispensing, 25 stenciling, dip coating, spraying, and dot shooting mechanisms.

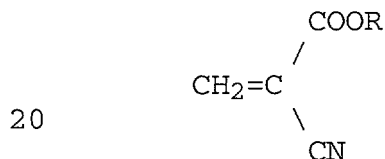
30 A total volume of the adhesive material applied to the leadframe 12 (or to the die 10) is dependent on the size of the die 10 and a desired thickness of the cured adhesive layer 20 (Figure 1B). A representative total volume of adhesive material for a 4.4 mm x 9.4 mm die can be from 0.0025 grams to 0.0011 grams. The resultant adhesive layer 20 (Figure 1B) can have a thickness of about 0.25 mils to 2 mils.

35 The adhesive material comprises an "instant curing adhesive". As used herein the term "instant curing adhesive"

refers to an adhesive material formulated to form adhesive bonds at a temperature of between 20°C to 30°C within 0.25 seconds to 60 seconds, and at ambient atmosphere. As used herein a "cured" material refers to a material wherein from 5 90% to 100% of the material has polymerized.

Suitable instant curing adhesives materials include cyanoacrylate adhesives and anaerobic acrylic adhesives. A preferred instant curing adhesive material comprises a cyanoacrylate adhesive. In general cyanoacrylate adhesives 10 are surface activated in that curing results from polymerization due to a catalytic interaction with the bonded surfaces. Adhesive stabilizers within the material can be used to stop polymerization at a desired thickness, or gap between the bonded surfaces.

15 The cyanoacrylate adhesive can be in the form of a monomer having the following general formula:



wherein R is a hydrocarbon group, for example an alkyl such as $\text{C}_n\text{H}_{2n+1}$. Suitable cyanoacrylate adhesives are commercially available from Loctite Corporation, Rocky Hill, 25 CT under the trademarks "410" or "416".

In general, the "410" and "416" cyanoacrylate adhesives are not formulated for electronics packaging. Accordingly, they must be modified with various other constituents and fillers to improve the thermal conductivity, mechanical 30 strength, electrical conductivity, dielectric strength, moisture resistivity, thermostability, or other characteristics of the adhesive layer 20 (Figure 1B) for a particular electronics packaging application.

Representative fillers for improving thermal 35 conductivity include SiO_2 , Al_2O_3 , AlN , Ag , Ni , and Fe . Representative fillers for improving mechanical strength include SiO_2 and SiC . Representative fillers for improving

electrical conductivity include Ag, Ni and polystyrene coated Ni. Representative fillers for improving dielectric strength and thermostability include SiO₂.

5 In addition, one or more catalysts can be added to the cyanoacrylate adhesive, or applied to the dice 10 or mounting paddles 12, to accelerate the polymerization of the adhesive layer 20 (Figure 1B). Suitable catalysts include weak acids and de-ionized (DI) water.

10 The method of the invention can also be practiced using an anaerobic acrylic adhesive. In general, anaerobic adhesives are fluids which cure in the absence of air but in the presence of metal by the free radical process. Typical anaerobic acrylic formulations are based on low molecular weight polyfunctional methacrylates. Representative
15 formulations are disclosed in U.S. Patent Nos. 4,309,526 and 4,295,909.

Immediately subsequent to the instant curing adhesive being dispensed on the mounting paddles 12, (or alternately on the die 10), the dice 10 can be placed in contact with the
20 adhesive material. The dispensing (step B) and placing steps (step C) can be performed using a system 22, which is shown schematically in Figure 4. The system 22 can include components of a conventional die attach machine configured for polymer bonding of dice to leadframes. One suitable die
25 attach machine is manufactured by ESC Manufacturing Company of Ivy Land, PA and is designated a model no. 9200.

The system 22 can include a leadframe feed mechanism 42 for manipulating the leadframes 14, and a vacuum tool 44 for manipulating the dice 10. In addition, the system 22 can
30 include a dispensing mechanism 46 for applying a desired volume of instant curing adhesive material to the mounting paddles 12 (Figure 1A) of the leadframes 14.

The system 22 can also include an optical alignment device 48 for aligning the dice 10 to the mounting paddles
35 12. Still further, the system 22 can include a die support platform 50 for placing the dice 10 in contact with the

mounting paddles 12 with a required pressure. Preferably this pressure can be in the range of 75 to 100 gm/die.

The polymerizing step (step D) occurs with the dice 10 placed in contact with the instant curing adhesive on the mounting paddles 12. With the adhesive sandwiched between the dice 10 and mounting paddles 12, a small amount of moisture contained on the mounting paddles 12 at ambient temperature, (i.e. condensed ambient humidity), can be sufficient to initiate anionic polymerization of the instant curing adhesive to form the cured adhesive layer 20 (Figure 1B). Polymerization of the a instant curing adhesive can be completed in from 0.25 to 60 seconds at an ambient temperature of from 20°C to 30°C. In this manner, the semiconductor dice 10 can be bonded to leadframe 14 without heating the dice 10 or leadframe 14. The adhesive layer 20 (Figure 1B) formed in this manner is sufficiently strong to withstand use in electronic equipment.

Referring to Figure 5, the method of the invention can also be performed for attaching LOC dice 10A, to a LOC leadframe 14A. The LOC leadframe 14A includes generally parallel and spaced lead fingers 38 which will subsequently be trimmed and formed into package leads, substantially as previously described. The lead fingers 38 also function to support the dice 10A during subsequent wire bonding and encapsulation processes. In addition to the lead fingers 38, the LOC leadframe 14A includes bus bars 32 for making electrical connections to bond pads 30 on the LOC dice 10A. The LOC leadframe 14A is formed such that the lead fingers 38 and bus bars 32 form separate die mounting areas 36 for each LOC die 10A.

Using the previously outlined method an instant curing adhesive, such as a cyanoacrylate adhesive or an anaerobic acrylic adhesive, can be applied to the lead fingers 38 in the die mounting areas 36. Preferably the instant curing adhesive includes an electrically insulating filler such as SiO₂. The insulating filler functions to increase the

dielectric strength of the adhesive layers 20C (Figure 6) and inhibits cross-talk between the lead fingers 38 in the completed package. In addition, a suitable catalyst, as previously described, can be added to the instant curing adhesive, or applied to the LOC dice 10A, to accelerate the polymerization process.

With the adhesive applied to the lead fingers 38, the LOC dice 10A can be placed in contact with the adhesive to form adhesive layers 20C (Figure 6) and bond the LOC dice 10A to the LOC leadframe 14A. A conventional LOC die attach system can be modified to form a system for attaching the LOC dice 10A substantially as previously described with system 22. Suitable LOC die attach systems are commercially available from ESC Manufacturing Company, Ivy Land, PA.

Example 1

A cyanoacrylate adhesive monomer sold by Loctite Corporation, Rocky Hill, CT under the trademark "416" was applied as a pattern of dots to the mounting paddle of a copper leadframe. A total volume of the adhesive monomer was about 0.005 grams. A semiconductor die having a 3 mm x 5.6 mm footprint was manually positioned upon the mounting paddle. Curing occurred within about 10 seconds to form an adhesive layer about 0.2 mils thick. The adhesion force of the resulting bond was measured to be about 1.7 kg. Application and curing of the adhesive was performed at a room temperature of between about 20 °C to 30 °C.

Example 2

A plurality of semiconductor dice having a footprint of 3 mm x 5.6 mm were secured to separate mounting paddles of a copper alloy leadframe using a model no. 9200 die attacher from ESC Manufacturing Company, Warrington, PA. Different cyanoacrylate adhesive monomers sold by Loctite Corporation, Rocky Hill, CT under the trademarks "410" and "416" were

applied and cured at a room temperature of from 20 °C to 30 °C. A total volume of adhesive was about 0.005 grams applied in various dot patterns to form adhesive layers about 0.2 mils thick. Both adhesives were found to cure in seconds
5 with a bonding force of about 1.7 kg/die.

Thus the invention provides a method for attaching semiconductor dice to a leadframe, an improved semiconductor die, and a system for performing the method. Although preferred materials have been described, it is to be
10 understood that other materials may also be utilized. As an example, although cyanoacrylate has been described as the instant curing adhesive, other instant curing adhesives, such as anaerobic, can also be utilized in accordance with the present invention. Furthermore, although the method of the
15 invention has been described with reference to certain preferred embodiments, as will be apparent to those skilled in the art, certain changes and modifications can be made without departing from the scope of the invention as defined by the following claims:

We claim:

1. A method for attaching a semiconductor die to a leadframe:

5 dispensing an adhesive material on the leadframe, said adhesive material formulated to cure in from about 0.25 seconds to 60 seconds;

 placing the die in contact with the adhesive material;
and

10 polymerizing the adhesive material to form a cured adhesive layer and bond the die to the leadframe.

2. The method as claimed in claim 1 wherein the adhesive material comprises a material selected from the
15 group consisting of cyanoacrylates, anaerobic acrylics or the like.

3. The method as claimed in claim 1 further comprising applying a catalyst to the leadframe, die or adhesive
20 material to initiate the polymerizing step.

4. The method as claimed in claim 1 wherein the leadframe includes a mounting paddle.

25 5. The method as claimed in claim 1 wherein the leadframe comprises a lead-on-chip leadframe.

6. A method for attaching a semiconductor die to a leadframe:

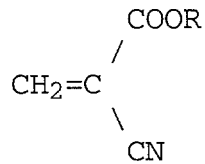
30 providing the leadframe with a mounting paddle;

 dispensing a cyanoacrylate adhesive material on the mounting paddle;

 placing the die in contact with the adhesive material;
and

35 polymerizing the adhesive material to form a cured adhesive layer and bond the die to the leadframe.

7. The method as claimed in claim 6 wherein the cyanoacrylate adhesive comprises a monomer with a formula:



10 wherein R comprises a hydrocarbon group.

8. The method as claimed in claim 6 wherein the dispensing step comprises a method selected from the group consisting of syringe dispensing, stenciling, dip coating, spraying, and dot shooting.

9. The method as claimed in claim 6 wherein dispensing the adhesive material comprises forming a pattern of dots.

10. The method as claimed in claim 6 further comprising adding a constituent to the adhesive selected from the group consisting of electrically conductive fillers, electrically insulating fillers, reinforcement fillers, catalyst fillers, heat conductive fillers, moisture resistance fillers and thermal stability fillers.

11. The method as claimed in claim 6 wherein the polymerizing step is initiated by condensed ambient humidity present on the leadframe.

12. A method for attaching a lead-on-chip semiconductor die to a lead-on-chip leadframe:

providing the leadframe with a plurality of lead fingers configured to form a die mounting area;

dispensing a cyanoacrylate adhesive material on the lead fingers in the die mounting area, said adhesive material comprising an electrically insulating filler;

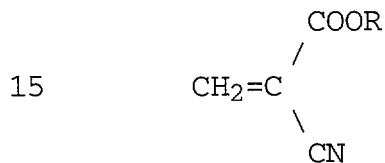
placing the die in contact with the adhesive material;
and

polymerizing the adhesive material to form a cured
adhesive layer and bond the die to the lead fingers.

5

13. The method as claimed in claim 12 further
comprising applying a catalyst to the lead fingers, die or
adhesive material prior to the placing step.

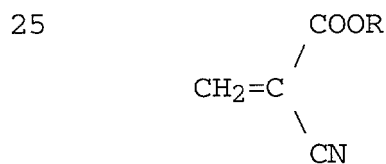
10 14. The method as claimed in claim 12 wherein the
cyanoacrylate adhesive comprises a monomer with a formula:



wherein R comprises a hydrocarbon group.

20 15. A method for attaching a semiconductor die to a
leadframe comprising:

providing an adhesive comprising a cyanoacrylate monomer
having a formula:



30 wherein R is a hydrocarbon group;

dispensing the adhesive on the leadframe or die;

pressing the die against the leadframe with the adhesive
compressed therebetween; and

35 curing the adhesive at a room temperature and ambient
atmosphere to form a cured adhesive layer and bond the die to
the leadframe.

16. The method as claimed in claim 15 further comprising applying a catalyst to the leadframe, die or adhesive prior to the pressing step.

5 17. The method as claimed in claim 15 further comprising providing the adhesive with a constituent selected from the group consisting of electrically conductive fillers, electrically insulating fillers, reinforcement fillers, catalyst fillers, heat conductive fillers, moisture resistant
10 fillers and thermal stability fillers.

18. The method as claimed in claim 15 wherein the leadframe comprises a mounting paddle for the die.

15 19. The method as claimed in claim 15 wherein the leadframe comprises a lead-on-chip leadframe.

20. The method as claimed in claim 15 wherein the dispensing step comprises a method selected from the group
20 consisting of syringe dispensing, stenciling, dip coating, spraying, and dot shooting.

21. A method for attaching a semiconductor die to a leadframe comprising:

25 providing an adhesive comprising an anaerobic acrylic;
dispensing the adhesive on the leadframe or die;
pressing the die against the leadframe with the adhesive compressed therebetween; and

curing the adhesive at a room temperature and ambient
30 atmosphere to form a cured adhesive layer and bond the die to the leadframe.

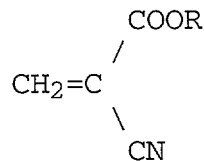
22. The method as claimed in claim 21 further comprising providing the adhesive with a constituent selected
35 from the group consisting of electrically conductive fillers, electrically insulating fillers, reinforcement fillers,

catalyst fillers, heat conductive fillers, moisture resistance fillers and thermal stability fillers.

23. A semiconductor package comprising:
5 a leadframe;
a semiconductor die bonded to a portion of the leadframe; and
an adhesive layer formed between the portion of the leadframe and the die to bond the die to the leadframe, said
10 adhesive layer formulated to cure at a room temperature in from about 0.25 seconds to 60 seconds.

24. The package as claimed in claim 23 wherein the adhesive layer comprises a material selected from the group
15 consisting of cyanoacrylates, anaerobic acrylics or the like.

25. The method as claimed in claim 23 wherein the adhesive layer comprises a cyanoacrylate monomer with a formula:



25 wherein R comprises a hydrocarbon group.

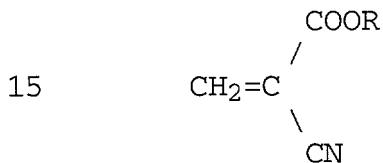
26. The package as claimed in claim 23 wherein the portion of the leadframe comprises a mounting paddle.

30 27. The package as claimed in claim 23 wherein the portion of the leadframe comprises a plurality of lead fingers of a lead-on-chip leadframe.

28. The package as claimed in claim 23 wherein the
35 adhesive layer further comprises a constituent selected from the group consisting of electrically conductive fillers, electrically insulating fillers, reinforcement fillers,

catalyst fillers, heat conductive fillers, moisture resistance fillers and thermal stability fillers.

- 5 29. A semiconductor package comprising:
a singulated portion of a leadframe including a mounting
paddle and a plurality of trimmed lead fingers;
a semiconductor die attached to the mounting paddle; and
an adhesive layer formed between the mounting paddle and
10 the die, said adhesive layer comprising a cyanoacrylate
monomer having a formula:

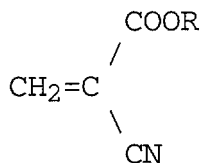


wherein R is a hydrocarbon group.

- 20 30. The package as claimed in claim 29 further
comprising a catalyst applied to the leadframe or die.

31. The package as claimed in claim 29 wherein the
adhesive layer further comprises a constituent selected from
25 the group consisting of electrically conductive fillers,
electrically insulating fillers, reinforcement fillers,
catalyst fillers, heat conductive fillers, moisture
resistance fillers and thermal stability fillers.

- 30 32. A semiconductor package comprising:
a singulated portion of a lead-on-chip leadframe
including a plurality of lead fingers;
a semiconductor die attached to the lead fingers; and
an adhesive layer formed between the lead fingers and
35 the die, said adhesive layer comprising a cyanoacrylate
monomer having a formula:



wherein R is a hydrocarbon group.

33. The package as claimed in claim 32 further comprising a catalyst applied to the leadframe or die.

34. The package as claimed in claim 32 wherein the adhesive layer further comprises a constituent selected from the group consisting of electrically conductive fillers, electrically insulating fillers, reinforcement fillers, catalyst fillers, heat conductive fillers, moisture resistance fillers and thermal stability fillers.

35. A system for attaching a semiconductor die to a leadframe comprising:

an adhesive selected from the group consisting of cyanoacrylate adhesives, anaerobic acrylics or the like;

a dispensing mechanism for dispensing the adhesive on the leadframe or die; and

a die attach mechanism for positioning and placing the die in contact with dispensed adhesive to form a cured adhesive layer for bonding the die to the leadframe.

36. The system as claimed in claim 35 wherein the dispensing mechanism comprises a mechanism selected from the group consisting of syringes, stenciling apparatus, dip coating apparatus, spraying apparatus, and dot shooting apparatus.

37. The system as claimed in claim 35 wherein the adhesive further comprises a constituent selected from the group consisting of electrically conductive fillers, electrically insulating fillers, reinforcement fillers,

catalyst fillers, heat conductive fillers, moisture resistance fillers and thermal stability fillers.

38. A system for attaching a semiconductor die to a
5 leadframe comprising:

an adhesive selected from the group consisting of cyanoacrylate adhesives, anaerobic acrylics or the like;

a lead on chip die attacher configured to align the die with a portion of the leadframe and to press the die against
10 the leadframe; and

a dispensing mechanism operably associated with the die attacher and configured to dispense the adhesive onto the portion of the leadframe or the die.

39. The system as claimed in claim 38 wherein the
15 leadframe comprises a lead on chip leadframe.

20

ABSTRACT

A method for attaching a semiconductor die to a leadframe is provided. Also provided are an improved semiconductor package, and a system for performing the method. The method includes applying an instant curing adhesive, such as a cyanoacrylate monomer or anaerobic adhesive, to the leadframe or die, and then polymerizing the adhesive at room temperature and ambient atmosphere, to form a cured adhesive layer between the die and lead frame. A catalyst can be applied to the leadframe, to the die or to the adhesive, to initiate polymerization. In addition, fillers can be added to the adhesive to improve various electrical and physical characteristics of the resultant adhesive layer. The system includes a dispensing mechanism for dispensing the instant curing adhesive on the leadframe or die, and a die attach mechanism for positioning and placing the die in contact with the dispensed adhesive.

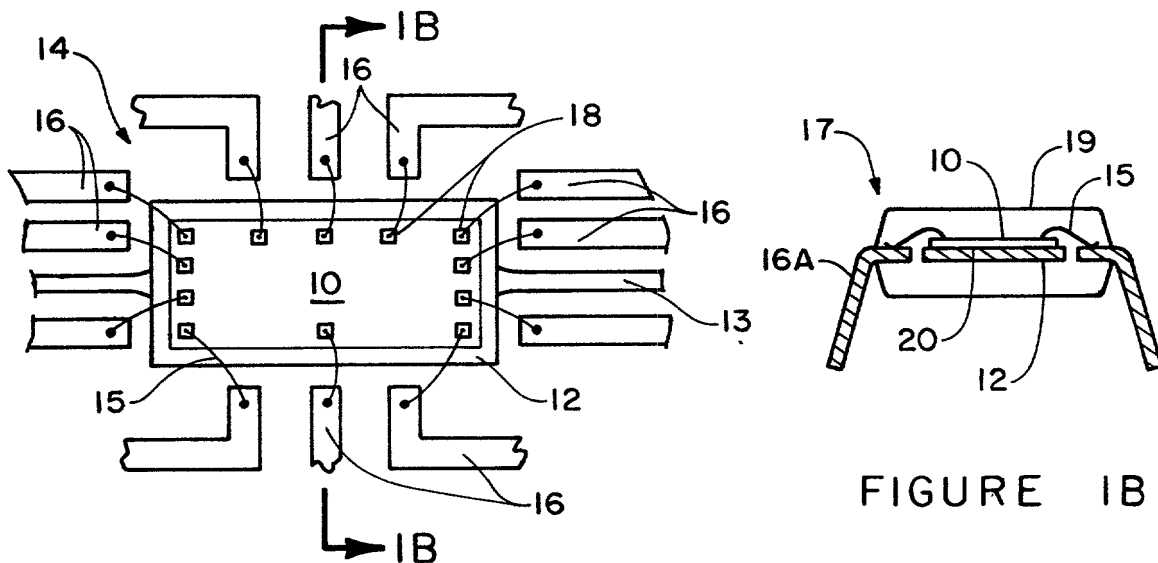


FIGURE 1A

FIGURE 1B

**PROVIDING A CONVENTIONAL SEMICONDUCTOR DIE
AND LEADFRAME, STEP A.**

**DISPENSING AN INSTANT CURING ADHESIVE
MATERIAL ON THE LEADFRAME (OR ALTERNATELY ON
THE DIE), STEP B.**

**PLACING THE DIE IN CONTACT WITH THE ADHESIVE
MATERIAL, STEP C.**

**POLYMERIZING THE ADHESIVE MATERIAL TO FORM A
CURED ADHESIVE LAYER AND BOND THE DIE TO THE
LEADFRAME, STEP D.**

FIGURE 2

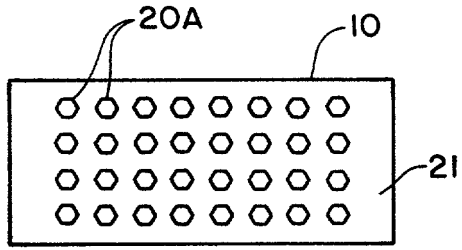


FIGURE 3A

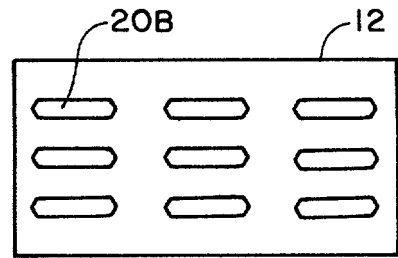


FIGURE 3B

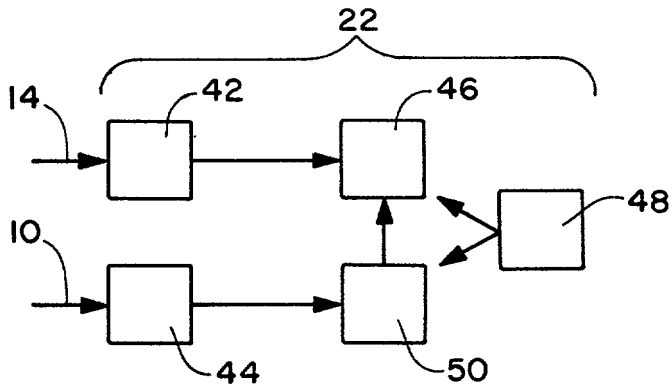


FIGURE 4

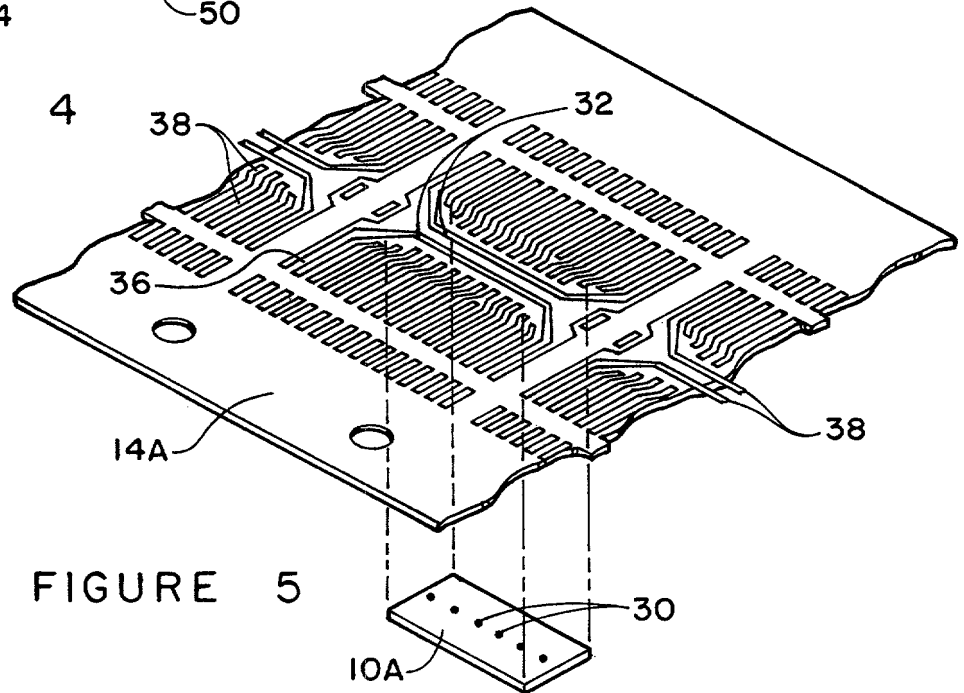


FIGURE 5

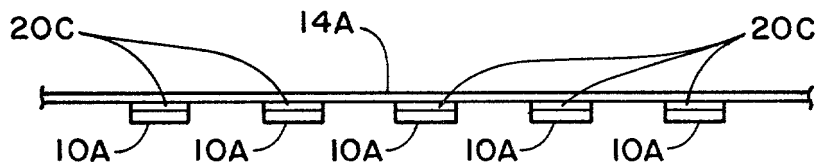


FIGURE 6

**DECLARATION AND POWER OF ATTORNEY
FOR PATENT APPLICATION
(Joint Inventors)**

We as the below named inventors, declare that:

Our residences, post office addresses and citizenships are as stated below next to our names.

We believe we are the original, first and joint inventors of the subject matter which is claimed and for which patent is sought on the invention entitled:

SEMICONDUCTOR DIE ATTACHMENT METHOD AND APPARATUS

the specification of which (check one)

☒ is attached hereto.

☐ was filed on _____ as Application
Serial No. _____.

and was amended on (if applicable) _____.

We hereby state that we have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

We acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, Sec. 1.56(a).

We hereby claim foreign priority under Title 35, United States Code, Sec. 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed: NONE

We hereby claim the benefit under Title 35, United States Code, Sec. 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, Sec. 11, we acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, Sec. 1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application: NONE

POWER OF ATTORNEY: We hereby appoint as our attorneys, STEPHEN A. GRATTON, Registration No. 28,418; MICHAEL L. LYNCH, Registration No. 30,871; and LIA M. DENNISON, Registration No. 34,095; with full power of substitution and revocation, to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith. All correspondence should be directed to:

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We hereby declare that all statements made herein of our own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Wherefore, we pray that Letters Patent be granted to us for the invention or discovery described and claimed in the foregoing specification and claims, declaration, power of attorney, and this petition.

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